Reading for exam2:

You must review the following topics:

- **CLN design**
  - K-maps with don't care conditions
  - Implicants, prime implicants, essential prime implicants, distinguished cells, ….
  - Finding minimal function of logic circuit
  - Timing diagram analysis and hazard

- **MSI**
  - MUX, Decoders, Pal
  - Using MUX, decoder, PAL to design logic functions

- **Cascading**
  - Cascade design of decoder
  - Cascade design of MUX

- **Modular design**
  - Identification of basic components
  - Combining basic components into larger design

Readings from the text book

- **Chapter 4**
  - Section 4.2
  - Section 4.3
    - Sections 4.3.1 - 4.3.7
  - Section 4.5

- **Chapter 5**
  - Section 5.1
    - 5.1.1, 5.1.2
  - Section 5.3
    - 5.3.1, 5.3.2
  - Section 5.4
    - 5.4.1 – 5.4.5
  - Section 5.7
    - 5.7.1, 5.7.2, 5.7.3
  - Section 5.9
    - 5.9.1, 5.9.2
  - Section 5.10
    - 5.10.1, 5.10.2, 5.10.3

- **Chapter 6** (This chapter presents examples of design. Read one or two examples)
  - 6.1.1, 6.1.2, 6.1.3