CSE300 (Spring 2006)
Special Topics in Computer Science and Engineering:
Research Topics in Computer Architecture

Instructor: Z. Jerry Shi, ITE 365, 486-0599, zshi@engr.uconn.edu.

Schedule: Monday and Wednesday 3:30 – 4:45pm, EII-321.

Office Hour: Monday 2:30 – 3:20pm.

Website: http://www.engr.uconn.edu/~zshi/course/cse300/ and WebCT.


Course goals: We will first review major components in modern microprocessor designs, which include but are not limited to instruction set architecture, superscalar pipeline, and memory hierarchy. We will then focus on the current research areas in computer architecture.

Structure: In the first half of the semester, the course mainly consists of lectures, which will cover the major topics in traditional computer architecture courses.

In the second half of the semester, students will participate more in the classroom and will work on a term project. Every student will read one or two recent research papers and make a presentation in the class. He or she should be able to understand the technique contents in the papers and explain them clearly to the class. At the end of the semester, students will also make a presentation on their project.

There will be a midterm exam, which will test your understanding of the topics covered in lectures.

Grades: Your grade for this course will be based on the following components:

- midterm: 30%
- project: 30%
- presentation: 20%
- others: 20%