

HOST-2009 IEEE International Workshop on HARDWARE-ORIENTED SECURITY and TRUST

Monday, July 27, Moscone Center, San Francisco, CA

Held in conjunction with DAC-2009 (<http://www.engr.uconn.edu/HOST>)

HOST 2009

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CALL FOR PAPERS AND PARTICIPATION

The emergence of a globalized, horizontal semiconductor business model raises a set of concerns involving the security and trust of the information systems on which modern society is increasingly reliant for mission-critical functionality. Hardware-oriented security and trust (HOST) issues span a broad range including threats related to the malicious insertion of Trojan circuits designed, e.g., to act as a 'kill switch' to disable a chip, to integrated circuit (IC) piracy, to attacks designed to extract encryption keys and IP from a chip, and to malicious system disruption and diversion. HOST covers security and trust issues in all types of electronic devices and systems such as ASICs, COTS, FPGAs, microprocessors/DSPs, and embedded systems. The mission of HOST is to provide a forum for the presentation and discussion of research that is of critical significance to the security of, and trust in, modern society's microelectronic-supported infrastructures.

The IEEE International Workshop on Hardware-Oriented Security and Trust (HOST 2009) is an open forum for discussions and innovations on all issues related to hardware security and trust. Paper presentations on topics given below will highlight the challenges faced with authenticating hardware for security and trust.

- Trojan detection and isolation
- Authenticating foundry of origin
- Side channel analysis/attacks
- Watermarking
- IP security/FPGA design security
- Cryptographic techniques for hardware security
- IC Metering
- Physical unclonable functions
- Embedded and distributed systems security
- Hardware intrusion detection and prevention
- Security engineering
- Scan-chain encryption
- IP Trust

To present at the workshop, submit an Acrobat (PDF) version of a paper or extended abstract of at least 1000 words online via EasyChair webpage at <http://www.easychair.org/conferences/?conf=host2009>. The maximum page limit is 8, double column, IEEE format (11 minimum font size). Each submission should include full name and address of each author, affiliation, telephone number, FAX and E-mail address. The accepted papers will appear in *ieeexplore*. The presenter should also be identified. Camera-ready papers for inclusion in the digest of papers will be due following the review cycle. Proposal for **Embedded Tutorials, Debates, Panel Discussions** or **"Spot-Light"** presentations describing industrial experiences are also invited.

AUTHOR'S SCHEDULE:

Submission of Paper/Extended Abstract:
Notification of Acceptance:
Camera Ready Paper:

April 17, 2009

May 22, 2009

June 5, 2009

Technical Program

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