

HOST-2010 IEEE International Workshop on HARDWARE-ORIENTED SECURITY and TRUST

Mon, June 14, 2010, Anaheim Convention Center, Anaheim, CA
Co-located with DAC-2010 (<http://www.engr.uconn.edu/HOST>)

HOST
2010

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CALL FOR PAPERS AND PARTICIPATION

The emergence of a globalized, horizontal semiconductor business model raises a set of concerns involving the security and trust of the information systems on which modern society is increasingly reliant for mission-critical functionality. Hardware-oriented security and trust (HOST) issues span a broad range including threats related to the malicious insertion of Trojan circuits designed, e.g., to act as a ‘kill switch’ to disable a chip, to integrated circuit (IC) piracy, and to attacks designed to extract encryption keys and IP from a chip. HOST covers security and trust issues in all types of electronic devices and systems such as ASICs, COTS, FPGAs, microprocessors/DSPs, and embedded systems. The mission of HOST is to provide a forum for the presentation and discussion of research that is of critical significance to the security of, and trust in, modern society’s microelectronic-supported infrastructures.

The IEEE **International Workshop on Hardware-Oriented Security and Trust** (HOST 2010) provides an open forum for discussions on all issues related to hardware security and trust. Papers and presentations that address any of the following “hot topics” are of high interest to the workshop. Papers addressing HOST issues outside of these areas will be considered equally relevant in the review process.

- Trojan Detection and Isolation
- Authenticating Foundry of Origin
- Side Channel Analysis/Attacks
- Watermarking
- FPGA Design Security
- Hardware focused Cryptography
- IC Metering
- Physical Unclonable Functions
- Embedded and Distributed Systems Security
- Hardware Intrusion Detection and Prevention
- Security Engineering
- Scan chain Encryption
- IP Security & Trust

To present at the workshop, submit an Acrobat (PDF) version of a paper or extended abstract of at least 1000 words online via EasyChair webpage at <http://www.easy-chair.org/conferences/?conf=host2010>. The maximum page limit is 8, double column, IEEE format (11 minimum font size). Each submission must identify the presenter and include full name and address of each author, affiliation, telephone number, FAX and E-mail address. Accepted papers will appear in IEEE Xplore. Proposal for **Embedded Tutorials, Debates, Panel Discussions** or “**Spot-Light**” presentations describing industrial experiences are also invited.

AUTHOR’S SCHEDULE:

Submission of Paper/Extended Abstract: **April 2, 2010**

Notification of Acceptance: **May 14, 2010**

Camera Ready Paper: **May 28, 2008**

Technical Program Submissions:

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General Information

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